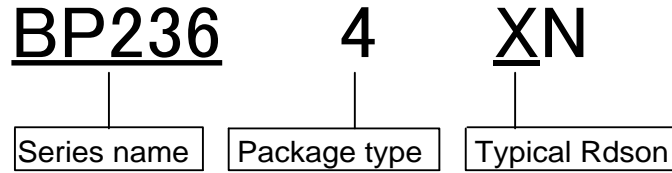


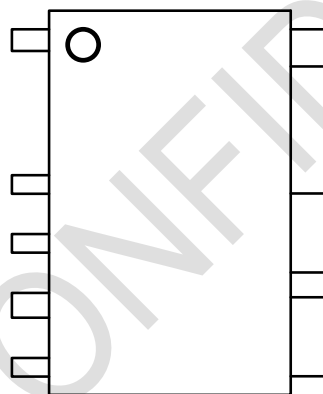
Naming rules



Ordering Information

Part Number	Package	Packing Method	Marking
BP2364XN	ASOP8	Tape 5,000 pcs/Reel	BP2364 XXXXXN ZZZZWWX

Pin Configuration and Marking Information



XXXXXX Lot Code
ZZZZ: Sign
WW: Week

Figure 2. Pin configuration

Pin Definition

Pin No.	Name	Description
1	GND	Ground.
2	ROVP	OVP set pin.
3	NC	No connection
4	HV	High voltage MOSFET drain terminal
5	VBUS	Rectifier bridge output positive pole
6,7	DRAIN	Internal HV Power MOSFET Drain.
8	CS	Current Sense Pin. Connect a sense resistor between this pin and GND pin.

Disclaimer

The information provided in this datasheet is believed to be accurate and reliable. However, Bright Power Semiconductor (BPS) reserves the right to make changes at any time without prior notice.

No license, to any intellectual property right owned by BPS or any other third party, is granted under this document. BPS provides information in this datasheet “AS IS” and with all faults, and makes no warranty, express or implied, including but not limited to, the accuracy of the information provided in this datasheet, merchantability, fitness of a specific purpose, or non-infringement of intellectual property rights of BPS or any other third party. BPS disclaims any and all liabilities arising out of this datasheet or use of this datasheet, including without limitation consequential or incidental damages.